

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Rajski et al.

Application No. 09/620,021

Filed: July 20, 2000

Confirmation No. 3823

For: CONTINUOUS APPLICATION AND
DECOMPRESSION OF TEST PATTERNS
TO A CIRCUIT-UNDER-TEST

Examiner: Phung M. Chung

Art Unit: 2133

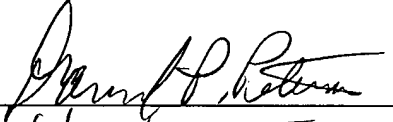
Attorney Reference No. 1011-54375-01

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for Applicants

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PURSUANT TO 37 C.F.R. § 1.97(b)(4)

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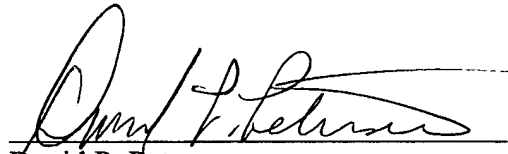
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Respectfully submitted,

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

Attorney Docket Number	1011-54375-01
Application Number	09/620,021
Filing Date	July 20, 2000
First Named Inventor	Rajski
Art Unit	2133
Examiner Name	Phung M. Chung

U.S. PATENT DOCUMENTS

Copies of U.S. Patent documents do not need to be provided, unless requested by the Patent and Trademark Office. For patents, provide the patent number and the issue date. For published U.S. applications, provide the publication number and the publication date. For unpublished pending patent applications, provide the application number and the filing date.

Examiner's Initials*	Cite No. (optional)	Number	Publication Date	Name of Applicant or Patentee

FOREIGN PATENT DOCUMENTS

Examiner's Initials*	Cite No. (optional)	Country	Number	Publication Date	Name of Applicant or Patentee

OTHER DOCUMENTS

Examiner's Initials*	Cite No. (optional)	
		Lew Yan Voon et al., "BIST Linear Generator based on Complemented Outputs," <i>IEEE VLSI Test Symp.</i> , pp. 137-142 (1992).
		Zacharia et al., "Two-Dimensional Test Data Decompressor for Multiple Scan Designs," <i>International Test Conference</i> , pp. 186-194 (1996).

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* Examiner: Initial if reference considered, whether or not in conformance with MPEP 609. Draw line through cite if not in conformance and not considered. Include copy of this form with next communication to applicant.